

# Clobber-NVM: Log Less, Re-execute More

Yi Xu<sup>1</sup>, Joseph Izraelevitz<sup>2</sup>, and Steven Swanson<sup>1</sup>

<sup>1</sup>University of California, San Diego

<sup>2</sup>University of Colorado, Boulder

## 1. Motivation

Non-volatile memory exposes persistent storage via a byte-addressable load/store interface. However, because the cache is volatile, cached updates to persistent state will be dropped after a power loss.

Failure-atomicity NVM libraries provide the means to apply sets of writes to persistent state atomically. These libraries provide failure-atomicity for specified code regions: for writes within a specified code region, all writes will survive a power loss and be written to NVM, or none will. These transaction-like, “all-or-nothing,” semantics make programming on NVM easier and hide architectural and caching details from programmers. Unfortunately, most current libraries impose significant overhead.

## 2. Limitations of the State of the Art

Most industrial failure atomicity systems use undo logging [7, 1]. In undo logging systems, where incomplete transactions are undone by applying undo log entries, the log entry must be persistent before the corresponding data update, which results in excessive persistence ordering constraints. In contrast, redo logging systems only have to persist the logs at transaction commit, but they need to intercept and redirect reads [8]. Prior attempts to reduce the logging cost either target specific data structures [2], double memory consumption by maintaining an additional working set shadow copy [6], impose limitations on programming model [4, 5] or relax the isolation model [3].

Among these systems, JUSTDO logging [4] proposed recovery-via-resumption. In contrast to undo logging, JUSTDO logging tracks enough program state to resume a failure-atomic operation at recovery, resuming execution from the interrupted instruction. Subsequent work in iDO logging [5] dramatically reduces program state logging frequency by exploiting idempotent code regions (a segment of code that does not overwrite its input). However, their runtime overhead remains quite high, and they strictly limit volatile data usage during a failure-atomic operation.

## 3. Key Insight

Clobber-NVM [9] ensures failure-atomicity by reexecuting any interrupted transactions at recovery and relies on a new logging method — clobber logging. Clobber logging’s key insight is that logging **only** overwritten inputs is sufficient to reexecute a transaction **with the exact same results**. We call a transaction input a *clobbered input* if it may be overwritten

within the transaction, and term this write a *clobber write*. Clobbered inputs are a problem for reexecution: If an input is clobbered during transaction execution, reexecuting the code will use a new value for the input.

The observation behind clobber logging is that clobber writes break reexecution — but undo logging them in a *clobber log* can preserve the clobbered inputs. **Values at other addresses never need to be logged** in clobber logging, since they will be overwritten upon reexecution. Because inputs which are not clobbered are expected to be available after a failure, and clobbered inputs are preserved in the undo log, clobber logging is sufficient for recovery.

Clobber logging drastically reduces the cost of failure atomicity. Clobber logging only logs a small set of selected values — the clobbered inputs, whereas undo logging must log before every writes in a transaction. Relative to prior recovery-via-resumption systems, clobber logging reduces log frequency. It only logs before a clobber write happens, while JUSTDO logs at every store and iDO logs at every idempotent region boundary and those regions tend to be small [5].

## 4. Main Artifacts

Our main artifact is Clobber-NVM, a combined compiler/runtime library that enforces failure atomicity using clobber logging.

The Clobber-NVM compiler extension, built on top of LLVM, is used to identify clobber writes within transactions using dependency analysis. It first identifies all possible input reads and all possible clobber writes as candidates for clobber logging. However, due to ambiguity in the analysis, it may conservatively over-identify clobber writes leading to excessive logging. Our compiler analysis subsequently refines the result through novel analysis propagation. The propagation reduces Clobber-NVM’s logging cost by avoiding logging at writes that will never actually be clobber writes at runtime.

To support volatile data usage inside transactions, Clobber-NVM uses a separate log (the *vlog*) to store minimal volatile data needed to re-construct a transaction’s volatile input.

The Clobber-NVM runtime manages the clobber log and the *vlog*. Our clobber log is built on PMDK’s undo log API. This design choice leaves Clobber-NVM’s clobber log very simple. Clobber-NVM manages the *vlog* directly.

The Clobber-NVM runtime also manages recovery of interrupted transactions after a crash. Figure 1 (top row) shows a transaction progressing through normal execution. A transaction interrupted by a crash follows a different path. As

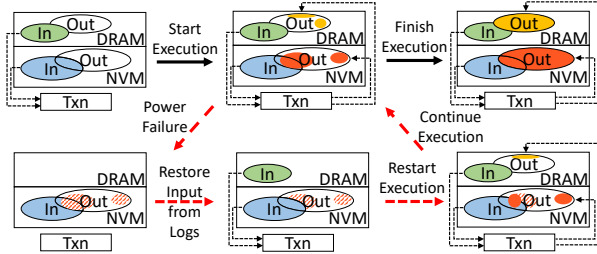


Figure 1: **Recovery Process of One Transaction.** The *In* and *Out* indicate addresses, instead of values — an output address may be updated several times during the transaction.

with normal execution, the transaction starts with initialized inputs and untouched outputs (Figure 1, top left), then progresses through execution by writing to some output addresses (Figure 1, top center), including clobber writes.

However, a power failure during execution drops the transaction to the recovery path. After the power loss, the transaction loses all volatile memory and some NVM outputs that still resided in the machine’s (volatile) caches (Figure 1, bottom left). At restart, Clobber-NVM restores both the volatile and clobbered inputs using the transaction’s logs, though the outputs may still be inconsistent (Figure 1, bottom center). With its inputs restored, the transaction is reexecuted from the beginning (Figure 1, bottom right).

Once the transaction executes past the point when the failure happened, the transaction has overwritten any incomplete outputs, erasing any inconsistencies caused by the power loss (Figure 1, top center). The transaction will continue to progress to completion and commit (Figure 1, top right).

## 5. Key Results

Figure 2 shows memcached performance on Clobber-NVM. We make the following observations. First, Clobber-NVM always outperforms Intel’s PMDK [7] and Mnemosyne [8], two prior failure atomicity systems. It provides, on average,  $1.7\times$  and  $2.1\times$  of PMDK and Mnemosyne performance, respectively. Second, Clobber-NVM outperforms PMDK and Mnemosyne more on write intensive workloads, because they involve more logging operations. Third, on single thread workloads, Clobber-NVM outperforms PMDK and Mnemosyne by up to  $1.74\times$  and  $2.15\times$ , respectively. Because this version of memcached uses a global lock to provide concurrency, Clobber-NVM and PMDK, as lock-based systems, scales poorly compared to Mnemosyne. We see their scalability and performance improve on data structures with a finer-grain locking scheme (see Figure 10 in the full paper).

## 6. Contributions

This paper makes the following contributions:

- It presents *clobber logging*, a novel, recovery-via-resumption strategy that reduces log size by only recording overwritten transaction inputs.

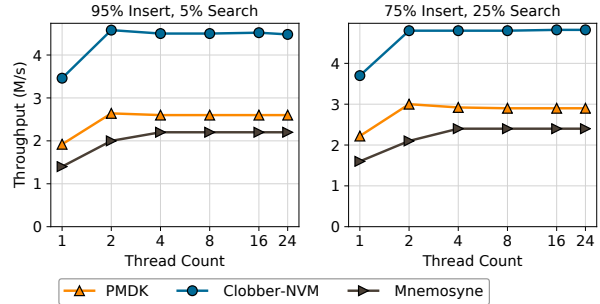


Figure 2: **Memcached Performance with 16-byte Keys and 64-byte Values**

- It presents a clobber logging compiler pass for identifying those transaction inputs that need to be logged.
- It introduces Clobber-NVM, a compiler-based failure-atomicity solution based on recovery-via-resumption.
- It demonstrates that Clobber-NVM’s performance compares favorably with the existing state-of-the-art systems, providing up to  $2.5\times$  improvement over Mnemosyne and  $2.6\times$  over Intel’s PMDK. It shows Clobber-NVM reduces log size by  $1.1\times$  to  $42.6\times$  and required expensive ordering fences by  $2.4\times$  to  $4.7\times$  relative to Intel’s PMDK.

## References

- [1] Dhruva R. Chakrabarti, Hans-J. Boehm, and Kumud Bhandari. Atlas: Leveraging locks for non-volatile memory consistency. In *Proceedings of the 2014 ACM International Conference on Object Oriented Programming Systems Languages & Applications, OOPSLA ’14*, pages 433–452, New York, NY, USA, 2014. ACM.
- [2] Nachshon Cohen, David T. Aksun, Hillel Avni, and James R. Larus. Fine-grain checkpointing with in-cache-line logging. In *Proceedings of the Twenty-Fourth International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS ’19*, page 441–454, New York, NY, USA, 2019. Association for Computing Machinery.
- [3] Jinyu Gu, Qianqian Yu, Xiayang Wang, Zhaoguo Wang, Binyu Zang, Haibing Guan, and Haibo Chen. Pisces: A scalable and efficient persistent transactional memory. In *Proceedings of the 2019 USENIX Conference on Usenix Annual Technical Conference, USENIX ATC ’19*, page 913–928, USA, 2019. USENIX Association.
- [4] Joseph Izraelevitz, Terence Kelly, and Aasheesh Kolli. Failure-Atomic Persistent Memory Updates via JUSTDO Logging. In *Proceedings of the Twenty-First International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS ’16*, pages 427–442, New York, NY, USA, 2016. ACM.
- [5] Qingrui Liu, Joseph Izraelevitz, Se Kwon Lee, Michael L Scott, Sam H Noh, and Changhee Jung. ido: Compiler-directed failure atomicity for nonvolatile memory. In *2018 51st Annual IEEE/ACM International Symposium on Microarchitecture (MICRO)*, pages 258–270. IEEE, 2018.
- [6] Amirsaman Memaripour, Anirudh Badam, Amar Phanishayee, Yanqi Zhou, Ramnatthan Alagappan, Karin Strauss, and Steven Swanson. Atomic in-place updates for non-volatile main memories with kamino-tx. In *Proceedings of the Twelfth European Conference on Computer Systems, EuroSys ’17*, page 499–512, New York, NY, USA, 2017. Association for Computing Machinery.
- [7] pmem.io. Persistent Memory Development Kit, 2017. <http://pmem.io/pmdk>.
- [8] Haris Volos, Andres Jaan Tack, and Michael M. Swift. Mnemosyne: Lightweight persistent memory. In *ASPLOS ’11: Proceeding of the 16th International Conference on Architectural Support for Programming Languages and Operating Systems*, New York, NY, USA, 2011. ACM.
- [9] Yi Xu, Joseph Izraelevitz, and Steven Swanson. Clobber-nvm: Log less, re-execute more. In *To appear in the Proceedings of International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS)*, 2021.